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REV	DESCRIPTION	DATE	ENGINEER
A	ORIGINAL RELEASE ECN #9477	9/18/2019	C CHURCH

VTAC INSERT	Signal Description	VHDCI	VTAC INSERT	Signal Description	VHDCI	VTAC INSERT	Signal Description	VHDCI	VTAC INSERT	Signal Description	VHDCI	VTAC INSERT	Signal Description	VHDCI	VTAC INSERT	Signal Description	VHDCI
2-1	GND	NOTE 7	4-1	GND	NOTE 7	6-1	GND	NOTE 7	8-1	GND	NOTE 7	10-1	GND	NOTE 7	12-1	GND	NOTE 7
2-2	DIO8_N	P1-15	4-2	DIO5_N	P1-44	6-2	DIO1_N	P1-38	8-2	PFI3_N	P1-33	10-2	GLOBAL CLK 0_N	P1-27	12-2	DIO12_N	P1-21
2-3	DIO8_P	P1-14	4-3	DIO5_P	P1-43	6-3	DIO1_P	P1-37	8-3	PFI3_P	P1-32	10-3	GLOBAL CLK 0_P	P1-26	12-3	DIO12_P	P1-20
2-4	GND	NOTE 7	4-4	GND	NOTE 7	6-4	GND	NOTE 7	8-4	GND	NOTE 7	10-4	GND	NOTE 7	12-4	GND	NOTE 7
2-5	GND	NOTE 7	4-5	GND	NOTE 7	6-5	GND	NOTE 7	8-5	GND	NOTE 7	10-5	GND	NOTE 7	12-5	GND	NOTE 7
2-6	DIO9_P	P1-49	4-6	DIO4_P	P1-8	6-6	DIO0_P	P1-2	8-6	PFI4_P	P1-67	10-6	DDC CLK OUT/PFI0_P	P1-61	12-6	DIO13_P	P1-55
2-7	DIO9_N	P1-50	4-7	DIO4_N	P1-9	6-7	DIO0_N	P1-3	8-7	PFI4_N	P1-68	10-7	DDC CLK OUT/PFI0_N	P1-62	12-7	DIO13_N	P1-56
2-8	GND	NOTE 7	4-8	GND	NOTE 7	6-8	GND	NOTE 7	8-8	GND	NOTE 7	10-8	GND	NOTE 7	12-8	GND	NOTE 7
3-1	GND	NOTE 7	5-1	GND	NOTE 7	7-1	GND	NOTE 7	9-1	GND	NOTE 7	11-1	GND	NOTE 7	NOTE 7 #1, 4, 5, 8	GND	P1-1, P1-4, P1-7, P1-10, P1-13, P1-16, P1-19, P1-22, P1-25, P1-28, P1-31, P1-34, P1-35, P1-36, P1-39, P1-42, P1-45, P1-48, P1-51, P1-54, P1-57, P1-60, P1-63, P1-66
3-2	DIO7_N	P1-47	5-2	DIO2_N	P1-6	7-2	DIO10_N	P1-18	9-2	PFI1_N	P1-30	11-2	DIO14_N	P1-24			
3-3	DIO7_P	P1-46	5-3	DIO2_P	P1-5	7-3	DIO10_P	P1-17	9-3	PFI1_P	P1-29	11-3	DIO14_P	P1-23			
3-4	GND	NOTE 7	5-4	GND	NOTE 7	7-4	GND	NOTE 7	9-4	GND	NOTE 7	11-4	GND	NOTE 7			
3-5	GND	NOTE 7	5-5	GND	NOTE 7	7-5	GND	NOTE 7	9-5	GND	NOTE 7	11-5	GND	NOTE 7			
3-6	DIO6_P	P1-11	5-6	DIO3_P	P1-40	7-6	DIO11_P	P1-52	9-6	PFI2_P	P1-64	11-6	DIO15_P	P1-58			
3-7	DIO6_N	P1-12	5-7	DIO3_N	P1-41	7-7	DIO11_N	P1-53	9-7	PFI2_N	P1-65	11-7	DIO15_N	P1-59			
3-8	GND	NOTE 7	5-8	GND	NOTE 7	7-8	GND	NOTE 7	9-8	GND	NOTE 7	11-8	GND	NOTE 7			


7. ALL GROUND PINS FROM THE VHDCI CONNECTOR ARE TIED TO PINS 1, 4, 5, AND 8 OF EACH VTAC INSERT.
6. PCB DESIGNED TO BE LOADED ON THE RIGHT WHEN VIEWED FROM THE VPC ITA MATING FACE.
5. ASSEMBLY RETAINED IN MODULE BY RETENTION INSERT, P/N 610151105, ONLY. 2 EXTRACTION TOOL REQUIRED FOR REMOVAL.
4. PCB ASSEMBLY COMES WITH PASS THROUGH INSERT, P/N 610151103, ASSEMBLED ONTO RIGHT ANGLE INSERT P/N 610151104. PLUG BLANK INSERTS, P/N 610151102, IN ADJACENT SLOTS BEFORE PLUGGING ASSEMBLY.
3. PCB ASSEMBLY REQUIRES 15 EMPTY VTAC MODULE POSITIONS.

2. MATERIALS:
- PCB:
- PCB = ISOLA MT-40
 - PCB PADS = GOLD IMMERSION
- VTAC INSERTS:
- VTAC CONTACT = ALLOY 7025
 - OUTER SHELL = BLUE/BLACK LCP
- VHDCI FML:
- HOUSING = THERMOPLASTIC
 - CONTACT = PHOSPHOR BRONZE
 - SHELL = STEEL

1. FOR APPLICATION USE AND CARE INFORMATION CONSULT VPC USERS GUIDE @ WWW.VPC.COM

NOTES:

CUSTOMER DRAWING

 **Virginia Panel Corporation**

PCB VTAC ITA TO
(1)SHLD VHDCI FML

RELATIVE CONNECTOR POSITIONS AND WIRE ROUTING ARE GENERIC AND MAY VARY WITH PINOUT

DIMENSIONS ARE SHOWN AS:
[MILLIMETERS]
INCHES

DWG.NO.
510171124

SCALE
1:1

CAGE CODE
18117

SHEET
1 OF 1

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